

**AMENDMENTS TO THE SPECIFICATION:**

Please substitute the following amended paragraph for the pending paragraph beginning on page 10, line 7:

Figure 4, which is identical to Figure 5 in the '755 patent incorporated by reference, shows the offset-correction concept of Figure 3 in the context of a device, such as generally shown in the '841—the '121 patent incorporated by reference, wherein separate video lines are multiplexed to form a single video line. In Figures 3 and 4 herein, like reference numerals (as well as voltage inputs, such as DCR) indicate like elements, with a modification that reference numerals followed by the letter O are specific to the odd-signal line 108O, and reference numerals followed by E indicate elements which are specific to the even video line 108E. However, it will be noticed that certain elements in the Figure 4 circuit, in particular RC circuit 140, line 142, and reference source 132, are common to both the odd and even video lines. Thus, the RC circuit 140 maintains a running average of dark photosensor signals of a combination of the dark photosensors on the odd video line 108O and also on the even video line 108E, even if, in certain embodiments, the two sets of dark photosensors are never otherwise connected. Also shown is an offset-removal circuit for each video line, indicated as 150O and 150E, which are peculiar to a particular practical embodiment. It will be noted that the function of circuits 150O and 150E can be used for multiplexing signals on the two separate video lines 112O and 112E onto a single output line, if  $\Phi_S$  is representative of the clock signal for each video signal being read through the video lines.